



ALTOS AND EXTREME DA TEAM TO CREATE ACCURATE INDUSTRY STANDARD CCS NOISE MODELS

Altos Liberate generates CCS noise-aware libraries for Extreme DA GoldTime

San Jose and Santa Clara, California – June 9, 2010 – Altos Design Automation Inc. and Extreme DA™ have jointly developed an essential signal-integrity (SI) design flow for integrated circuit (IC) designs manufactured at process nodes of 65-nanometers (nm) and below. Mutual customers of both companies have verified successful SI static timing analysis using Altos' Liberty™ models with the Extreme DA GoldTime™ analyzer.

SI Timing Analysis Required

A SI noise-aware design flow is required for effectively managing the crosstalk delay and glitch noise found in the dense and complex ICs manufactured in current nanometer-scale silicon technology nodes. The SI-aware flow, featuring advanced current source models (CCS), enables designers to accurately account for these noise effects. It eliminates the need for designers to use separate analysis tools and proprietary noise models that require lengthy characterization.

The accuracy of the underlying cell library used by GoldTime to predict the effects of crosstalk delay and glitch noise on a design's timing and yield is essential for success. The development of this verified flow has qualified Altos as an Extreme DA-approved source for the SI-aware libraries required by GoldTime.

"We have worked with Extreme DA to validate Liberate's CCS noise-aware timing models with GoldTime timing analysis," said Jim McCanny, Altos CEO. "We are committed to ongoing support of any advanced requirements that GoldTime users may have in the future."

"Noise accurate timing analysis relies on detailed characterization of the cell libraries used by designers," said Mustafa Celik, president and CEO of Extreme DA. "Our collaboration with Altos has produced a powerful, verified SI flow that can improve IC quality, increase yields, and accelerate time to market."

Availability and Pricing

Extreme DA GoldTime for use with Altos Variety and Liberate models is available now from Extreme DA. Pricing varies depending on configuration. Altos Variety and Liberate approved libraries for Extreme DA GoldTime are available now from Altos.

About Altos Liberate

Liberate is an ultra-fast standard cell and I/O library creator. It generates electrical cell views for timing, power and signal integrity including advanced current source models (CCS and ECSM). It uses a unique “inside view” approach where each cell undergoes a pre-characterization circuit analysis that determines all the necessary stimulus and internal logic states to ensure a complete, accurate and highly efficient characterization of that cell. Liberate supports complex cells including those required for low power design such as state retention flip-flops, level shifters, power switches and MTCMOS cells with sleep modes.

About Altos

Altos Design Automation provides ultra-fast, fully automated, characterization technology for the creation of library views for timing, signal integrity and power analysis and optimization. Altos advanced modeling solutions are used by both corner-based and statistical-based design implementation flows to reduce time to market and improve yield.

Privately held, Altos was founded in 2005 in Santa Clara, CA. Its corporate headquarters is at 4020 Moorpark Ave., Suite 100, San Jose, CA 95117. Telephone: (408) 980-8056. On the Web at: <http://www.altos-da.com>

About GoldTime

GoldTime by Extreme DA, is the new-generation timing analysis technology that delivers 5X better speed and capacity. With its new from-the-ground-up architecture, designers can sign-off with certainty and achieve faster timing closure. Whether verifying a current generation design across corners or doing a statistical analysis to optimize the performance, power and yield for 40nm ICs, GoldTime delivers the answers while the competition is still figuring out the results.

About Extreme DA

Headquartered in Santa Clara, Calif., venture-funded Extreme DA develops and licenses software products that provide sign-off analysis and improve the performance and yield of nanometer integrated circuits prior to manufacture. The company's investors include Foundation Capital, IT-Farm Corporation, and Lanza techVentures. For the latest news and information on Extreme DA, visit www.extreme-da.com or write to info@extreme-da.com.

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