



FOR IMMEDIATE RELEASE

Extreme DA Announces Donation of Kindle DX and Results of IC Design Survey Give-Away

Santa Clara, Calif. — Dec. 8, 2009 — Extreme DA™ today announced the winner of its 2009 integrated circuit (IC) design survey give-away. Extreme DA collects the opinions of design engineers, so they can influence the development of sign-off timing analysis in future versions of the GoldTime™ product. Everyone who had completed the survey, at <http://www.extreme-da.com/survey.html>, was entered into the prize drawing. A lucky engineer from ARM Inc., USA, was picked at random and was awarded a Kindle DX wireless reading device. The winner, who asked to remain anonymous, requested that the prize be donated to a deserving school. Extreme DA has chosen a public school in San Jose, Calif. to support its reading program for elementary school students.

Dale Jones, principal of Discovery Charter School, said, "The Kindle provides access to quality literature and informational texts for students who struggle with the mechanics of reading. It can help prevent academic deficiencies while motivating students to put in the hard hours of practice required to learn to read well. Our staff and students really appreciate the generous donation."

Selected Results of the IC Designer Survey

In describing their current projects, respondents indicated that ASIC designs were the most common being implemented, with custom / semi-custom chips coming in second:

ASICS: 49%	IP: 5%
Processors: 15%	Custom and Semi-custom chips: 34%

For design clock speeds, two-thirds were above the frequency of 400 MHz:

800-1600 MHz: 35%	100-400 MHz: 23%
400-800 MHz: 32%	Other: 6%

In terms of design size, the most common is designs featuring 1-5 million cells:

20 Mcells and greater: 26%	1-5 Mcells: 33%
5-20 Mcells: 15%	Less than 1 Mcell: 26%

Survey respondents also noted the manufacturing process node they were planning to use for their current designs. A large majority of the designs target process nodes of 45-nanometers and below:

45 nm or below: 62%

90 nm: 10%

70/65 nm: 21%

Other: 7%

Top Challenges

Respondents also identified the following as the top three challenges to achieving Timing Sign-off:

1. Accuracy of results: 24%
2. Speed of analysis: 22%
3. Process variations: 14%

For more information about the survey, contact Ruben Molina, Director of Technical Marketing at Extreme DA.

About GoldTime — The New Standard in Sign-off Timing

GoldTime by Extreme DA, is the new-generation timing analysis technology that delivers 5X better speed and capacity. With its new from-the-ground-up architecture, designers can sign-off with certainty and achieve faster timing closure. Whether verifying a current generation design across corners or doing a statistical analysis to optimize the performance, power and yield for 40nm ICs, GoldTime delivers the answers while the competition is still figuring out the results.

About Extreme DA

Headquartered in Santa Clara, Calif., venture-funded Extreme DA develops and licenses software products that provide sign-off analysis and improve the performance and yield of nanometer integrated circuits prior to manufacture. The company's investors include Foundation Capital, IT-Farm Corporation, and Lanza techVentures. For the latest news and information on Extreme DA, visit www.extreme-da.com or write to info@extreme-da.com.

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Contact :

Jean Armstrong, PR Counsel for Extreme DA
Armstrong and Associates, Inc.
503-477-5434

Ruben Molina, Director of Technical Marketing
Extreme DA
408-588-1112