Visit Extreme DA at the 46th DAC: Discover 'No Compromise' Timing Analysis and Enter to Win a Kindle DX

Santa Clara, Calif. — July 23, 2009 — In booth 1710 at the 46th Design Automation Conference (DAC), Extreme DA™ will exhibit the newest version of GoldTime and its no compromise timing analysis. GoldTime improves the performance and yield of nanometer integrated circuits (ICs) with unmatched speed and analysis capacity and addresses process variations in semiconductor designs.

Extreme DA in Booth 1710

- Extreme DA will provide informal demonstrations of the newest version of GoldTime, and will focus on the features benefits of full-chip signal integrity (SI) analysis and on parametric on-chip variation (OCV) analysis in an easy-to-use flow.
- Everyone who views a presentation will automatically be entered to win a Kindle DX wireless reading device.
- Extreme DA technologists will be on hand to talk with attendees about IC designs and how to achieve faster timing sign-off.

Extreme DA in Suite 1710

- Extreme DA will host private meetings in its suites to demonstrate the new features and benefits of GoldTime and share its technology roadmap.
- An exclusive one-time presentation by Fumihiro Minami, Leader for VA group, STARC Japan on the topic "**Timing Margin Reduction using GoldTime SSTA**" to be given on Monday, July 27, from 1:30 to 2:00 pm.
- Suite presentations will include:

  GoldTime — The Fastest Timing Analysis Without Compromise
  Whether your design contains 1 million or 100 million cell instances, GoldTime delivers the performance to reduce ECO turn around time, the need for expensive compute resources and speeds signoff analysis. Come and see the results we deliver.

  GoldTime Parametric OCV — The Practical Approach to SSTA
  Let's face it: only the largest companies can afford the cost and manpower to develop the libraries for full statistical timing analysis. Extreme DA introduces Parametric OCV, which enables statistical analysis without a huge upfront investment. Use your corner libraries and existing signoff flows while leveraging the statistical analysis of cell and interconnect variation. Let Extreme DA engineers show you the practical way to statistical analysis.

  GoldTime MXO — Multi-Mode / Multi-Corner Analysis and Optimization
  Without statistical analysis designers are forced to run more corners than ever before. The complexity of numerous functional and test modes for nanometer ICs makes signoff an overwhelming exercise in analysis and optimization. GoldTime MXO reduces this burden by providing consolidated reporting across multiple mode/corner analyses. Not only are worst-case violators visible across the space of

operating conditions and modes, but MXO also automatically fixes holdtime violations where appropriate. Unlike other solutions, get the details on why GoldTime MXO is one of the hottest and most frequently requested offerings at Extreme DA.

- Prospective Extreme DA customers and partners may schedule suite presentations with Extreme DA executives and technologists in the suite by registering at http://extreme-da.com/DAC2009-signup.html.
- Editors and analysts may set up briefings with Extreme DA executives and technologists, by contacting Jean Armstrong, Armstrong Kendall, Inc. at (503) 477-5434 or Jean@AAA-PR.com.

About Extreme DA

Headquartered in Santa Clara, Calif., venture-funded Extreme DA develops and licenses software products that provide sign-off analysis and improve the performance and yield of nanometer integrated circuits prior to manufacture. The company's investors include Foundation Capital, IT-Farm Corporation, and Lanza techVentures. For the latest news and information on Extreme DA, visit www.extreme-da.com or write to info@extreme-da.com.

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